

MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21
II B.TECH I SEMESTER - SEC– A

Class In Charge: Ms. J. Mary Angel Asha Lath

With effect from: 03-09-2020

Room No: EB - 214

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10–01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00
MON	EDC	DSD	LSE	B R E A K	SPT Lab		
TUE	EDC(B1)/DSD (B2)Lab				MC	NT	SEM
WED	LSE	NT	EFAE		EDC	GATE	LIB
THU	DSD	EFAE	LSE		EDC(B2)/DSD (B1)Lab		
FRI	NT	EDC	DSD		EFAE	MC	LSE
SAT	NT	DSD	EDC		Holistic Activities		

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. K. S. Srinivsa Rao
LSE	18BIO101	Life Sciences for Engineers	Dr. Chandramohan K
NT	18ECE101	Network Theory	Dr. Brijesh Kumar Singh
DSD	18ECE102	Digital System Design	Dr. Sambasiva Rao Kambala
EDC	18ECE103	Electronic Devices and Circuits	Ms. J. Mary Angel Asha Latha
MC	Mandatory Course- (Indian Constitution)		Ms. J. Mary Angel Asha Latha
Seminar			Dr. Satrugan Kumar
GATE			Dr. Brijesh Kumar Singh
LABORATORY			
SPT Lab	18ENG201	English Communication-Speaking and Listening	Dr. Akhil/ Dr. Sarah
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Sambasiva Rao Kambala / Mr. V. Prakash Kumar
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Ms. J. Mary Angel Asha Latha / Ms. Akshatha S

Time Table Coordinator

HOD

Principal

MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21
II B.TECH I SEMESTER - SEC– B

Class In Charge: Mr. G. Subbarao

With effect from: 03-09-2020

Room No: EB - 213

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10–01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00
MON	EFAE	MC	LSE	B R E A K	DSD (B1) / EDC (B2)		
TUE	DSD	EFAE	EDC		LSE	NT	GATE
WED	NT	DSD	EDC		EFAE	LSE	LIB
THU	DSD (B2) / EDC (B1)				DSD	MC	SEM
FRI	LSE	NT	EDC		SPT Lab		
SAT	DSD	EDC	NT		Holistic Activities		

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. K. S. Srinivsa Rao
LSE	18BIO101	Life Sciences for Engineers	Dr. Venkateswarlu Ch
NT	18ECE101	Network Theory	Mr. B. Subbarayudu
DSD	18ECE102	Digital System Design	Dr. Chandramauleshwar Roy
EDC	18ECE103	Electronic Devices and Circuits	Dr. Ranjan Kumar Mahapatra
MC	Mandatory Course- (Indian Constitution)		Mr. G. Subbarao
Seminar			Dr. Bibhuti Bhusan Pradhan
Gate			Dr. Ranjan Kumar Mahapatra
LABORATORY			
SPT Lab	18ENG201	English Communication-Speaking and Listening	Dr. Zareena/ Dr. Lakshmi Devi
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Chandramauleshwar Roy / Ms. S. Ayesha Tanveer(Mon)/ Mr. B. Subbarayudu(Thu)
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Dr. Ranjan Kumar Mahapatra/ Mr. E. Ramesh(Mon)/ Ms. J. Mary Angel Asha Latha(Thu)

Time Table Coordinator

HOD

Principal

MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21
II B.TECH I SEMESTER - SEC– C

Class In Charge: Mr. E. Ramesh

With effect from: 03-09-2020

Room No: EB - 212

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10–01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00
MON	DSD(B1) / EDC (B2)			B R E A K	NT	DSD	EFAE
TUE	EDC	LSE	DSD		EFAE	GATE	LIB
WED	LSE	EDC	NT		EDC (B1) / DSD (B2)		
THU	NT	DSD	EDC		LSE	MC	SEM
FRI	SPT Lab				LSE	EDC	EFAE
SAT	DSD	NT	MC		Holistic Activities		

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. K. S. Srinivsa Rao
LSE	18BIO101	Life Sciences for Engineers	Dr. Ashok Kumar Das
NT	18ECE101	Network Theory	Mr. V. Prakash Kumar
DSD	18ECE102	Digital System Design	Dr. Kumar Manoj
EDC	18ECE103	Electronic Devices and Circuits	Mr. E. Ramesh
MC	Mandatory Course- (Indian Constitution)		Mr. E. Ramesh
Seminar			Dr. Dheeren Ku Mahapatra
Gate			Dr. Kumar Manoj
LABORATORY			
SPT Lab	18ENG201	English Communication-Speaking and Listening	Dr. Tushar/ Dr. Akhil
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Kumar Manoj / Ms. S. Ayesha Tanveer
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Mr. E. Ramesh/ Mr. V. Prakash Kumar

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MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21

II B.TECH I SEMESTER - SEC- D

Class In Charge: Mr. B. Subbarayudu

With effect from: 03-09-2020

Room No: EB - 211

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10–01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00
MON	EDC(B1)/ DSD(B2)			B R E A K	DSD	LSE	LIB
TUE	DSD	EDC	EFAE		EDC	NT	GATE
WED	LSE	NT	DSD		NT	EFAE	
THU	SPT Lab				EDC	MC	SEM
FRI	NT	LSE	DSD		EDC(B2)/ DSD(B1)		
SAT	EFAE	MC	EDC		Holistic Activities		

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. N. Shesadri
LSE	18BIO101	Life Sciences for Engineers	Dr. Sumanta Sahoo
NT	18ECE101	Network Theory	Dr. Sambasiva Rao Kambala
DSD	18ECE102	Digital System Design	Mr. B. Subbarayudu
EDC	18ECE103	Electronic Devices and Circuits	Dr. Jitendra Bahadur
MC	Mandatory Course- (Indian Constitution)		Mr. S. Tamil Selvan
Seminar			Dr. Chandramauleshwar Roy
Gate			Dr. Jitendra Bahadur
LABORATORY			
SPT Lab	18ENG201	English Communication-Speaking and Listening	Sarah/Ganesh
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Satrugan Kumar/ Mr. B. Subbarayudu
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Dr. Jitendra Bahadur / Dr. Brijesh Kumar Singh

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MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-2021
II B.TECH I SEMESTER - SEC– E

Class In Charge: Ms. S. Ayesha Tanveer

With effect from: 03-09-2020

Room No: EB - 210

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10–01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00
MON	SPT Lab			B R E A K	NT	EDC	LIB
TUE	DSD	MC	NT		EFAE	EDC	GATE
WED	EDC (B2) /DSD (B1)				DSD	NT	LSE
THU	EFAE	DSD	LSE		EDC (B1) / DSD (B2)		
FRI	LSE	EDC	NT		MC	SEM	
SAT	EDC	DSD	EFAE		Holistic Activities		

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. M. Venkata Ramana
LSE	18BIO101	Life Sciences for Engineers	Dr. Mainak Ganguly
NT	18ECE101	Network Theory	Dr. Sankata Bhanjan Prusty
DSD	18ECE102	Digital System Design	Mr. R. Kiran Kumar
EDC	18ECE103	Electronic Devices and Circuits	Dr. Remashan Kariyadan
MC	Mandatory Course- (Indian Constitution)		Ms. S. Ayesha Tanveer
Seminar			Dr. Ranjan Kumar Mahapatra
Gate			Dr. Gajendra Sharma
LABORATORY			
SPT Lab	18ENG201	English Communication-Speaking and Listening	Dr. Ganesh/ Dr. Zareena
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Sharwan Ram/ Dr. Satrugan Kumar(Wed) /Dr. Sambhudutta Nanda(Thu)
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Dr. Remashan Kariyadan/ Ms. J. Mary Angel Asha Latha(Wed)/ Dr. Ranjan Kumar Mahapatra(Thu)

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MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2020-21
II B.TECH I SEMESTER - SEC– F

Class In Charge: Mr. V. Prakash Kumar

With effect from: 03-09-2020

Room No: EB - 217

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10–01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00
MON	DSD	LSE	EDC	B R E A K	DSD	MC	LIB
TUE	LSE	EDC	NT		EDC (B1)/ DSD (B2)		
WED	EFAE	NT	DSD		LSE	SEM	
THU	NT	EFAE	EDC		SPT Lab		
FRI	EDC (B2)/ DSD (B1)				EDC	MC	GATE
SAT	NT	DSD	EFAE		Holistic Activities		

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. N. Shesadri
LSE	18BIO101	Life Sciences for Engineers	Dr. A. Selvaganapathy
NT	18ECE101	Network Theory	Mr. S. Tamil Selvan
DSD	18ECE102	Digital System Design	Dr. Sambhudutta Nanda
EDC	18ECE103	Electronic Devices and Circuits	Dr. Remashan Kariyadan
MC	Mandatory Course- (Indian Constitution)		Mr. V. Prakash Kumar
Seminar			Dr. P. Sarwesh
Gate			Dr. Remashan Kariyadan
LABORATORY			
SPT Lab	18ENG201	English Communication-Speaking and Listening	Dr. Suraj/ Dr. Tushar
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Sambhudutta Nanda/ Dr. Ravi Tiwari
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Mr. S. Tamil Selvan/ Ms. S. Ayesha Tanveer(Tue)/ Ms. Akshatha S(Fri)

Time Table Coordinator

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Principal